



Richard Sbuell

27.07.1981, Leoben

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PROFILE:

Consultant in the field of electronic system development with highly specialized know-how in the field of integrated mixed signal circuit design and more than 10 years' experience in industrial project work. Awarded for outstanding academic excellence.

EDUCATION:

2000 – 2004 **University of Applied Sciences in Kapfenberg**

- Graduate Dipl. Ing. (FH)
- Field: Microelectronics
- Grade point average [A]

1995 – 2000 **College for Electrical Engineering in Kapfenberg**

- A Level Exam: Mathematics [A], English [A], Electrical power engineering [A], German [A], Engineering project [A]
- Graduated with excellence, received medal for outstanding scholarly accomplishments.

EXPERIENCE:

2017 – Now **Supersonic Vienna**

CEO
www.supersonic.at

Responsibilities and achievements:

- Design and manufacturing of high end audio systems
- Restoration of vintage audio and video gear

2019 – Now **FH Technikum Wien**

Lecturer
www.technikum-wien.at

Lectures:

- Electrical Measurement Laboratory
- Electronics Laboratory

2009 – Now **Handelsgericht Wien (SDG-Liste)**

Expert in the field of Information Technology
www.sv.sbuell.com

- 68.11 Personal Computer
- 66.15 Circuits- and elements of electronic devices

2005 – Now **sbuell engineering GmbH**

CEO
www.engineering.sbuell.com

Responsibilities and achievements:

- Mixed signal ASIC design down to 40nm
More than 20 patents in the field of integrated RFID Circuits
- Research in the field UHF switching GaN DCDC converters
- Continuous work in the field of embedded systems

2006 – 2017 **FH Joanneum GmbH**

Scientific staff member
Responsible for electronic development projects and lecturing.

Lectures:

- Analogue Circuit IC-Design
- Mixed Signal Circuit IC-Design

2003 – 2005 **Infineon Technologies AG**

Chip design engineer- Analogue mixed signal
Member of the smart card security controller core-design team.

Responsibilities and achievements:

- Concept, modelling, design and layout of analogue integrated circuits (180nm)
- Responsible for student- internships
- Developed a system for current profile analysis of a contact-less chip card
- Developed a platform for passivation leak-proof test for Finger-Tip© Sensors

2002 **FH Joanneum GmbH**

Project assistant
Member of the mobile medical monitoring project team.

Responsibilities and achievements:

- Developed lower layers of a Bluetooth protocol stack in C language
- Designed a SOC evaluation platform for a biometrical measurement system
- Developed a system for electromagnetic field strength measurement

INTERESTS:

- Sports (Basketball, Jogging, Wakeboarding, Yoga)
- Music (production of electronic music)
- Cooking

COMPUTER SKILLS:

- Operating Systems: Microsoft Windows (Expert), Linux & UNIX (User)
- Office Tools: Microsoft Office
- Picture Publishing: PageMaker, QuarkXPress, Publisher, Photoshop
- Programming: C, VHDL, Java
- Technical: Mentor Graphics Board Station, Cadence Virtuoso, Matlab, Mathcad, Lab-Windows, OrCAD, Eagle